

FIG. 1

COPY OF PAPERS  
ORIGINALLY FILED

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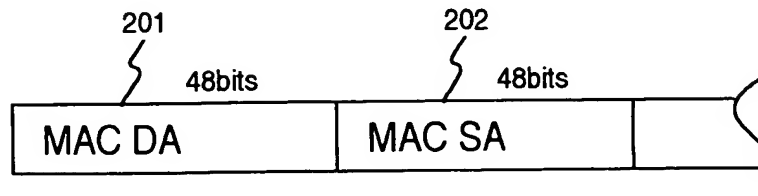


FIG. 2

COPY OF PAPERS  
ORIGINALLY FILED

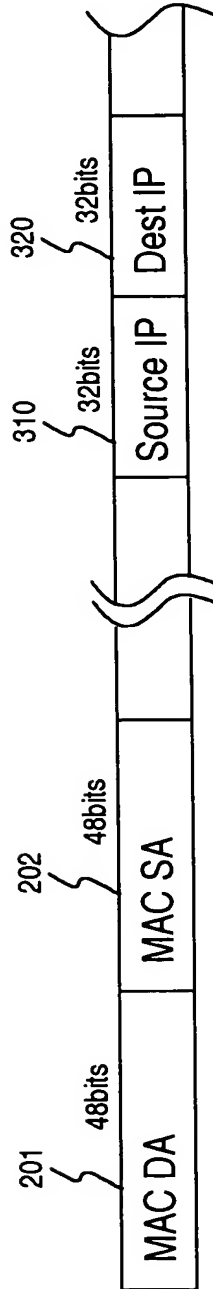


FIG. 3

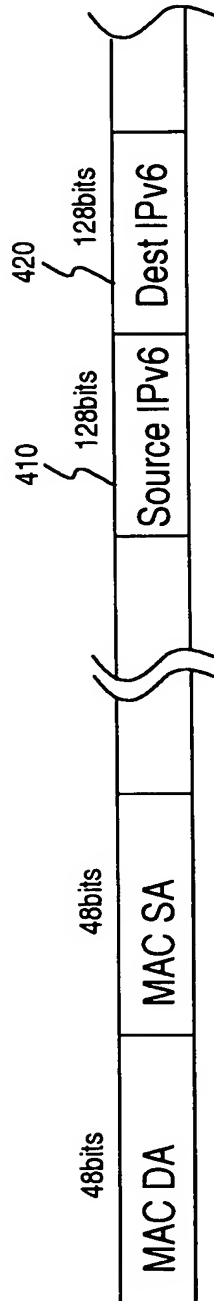


FIG. 4

COPY OF PAPERS  
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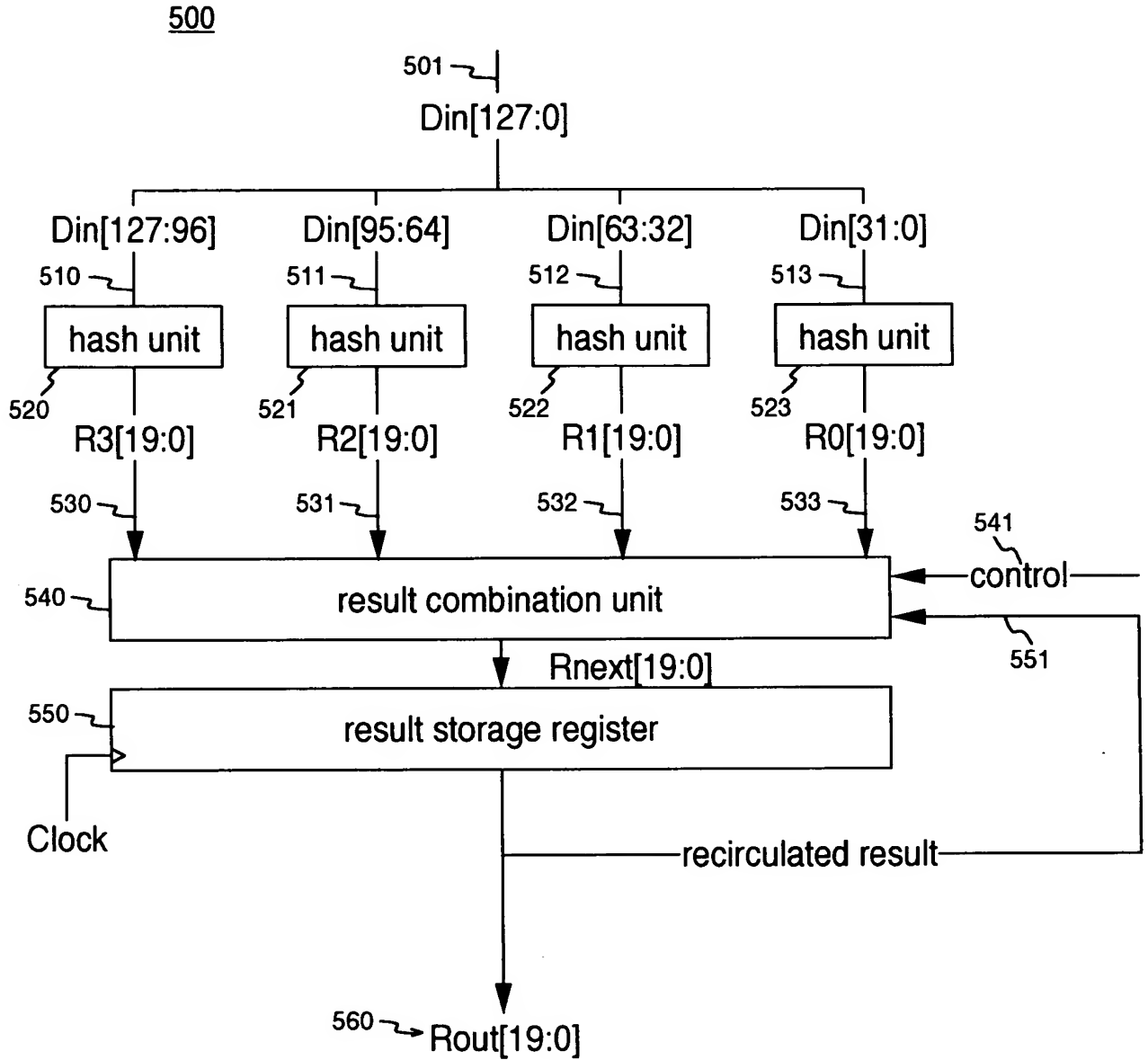


FIG. 5

```
if (control == 0)
{
    Rnext = R3 XOR R2 XOR R1 XOR R0;
}
else
{
    Rnext = R3 XOR R2 XOR R1 XOR R0 XOR Rout;
}
Rout <= Rnext on each positive clock edge
```

FIG. 6

700

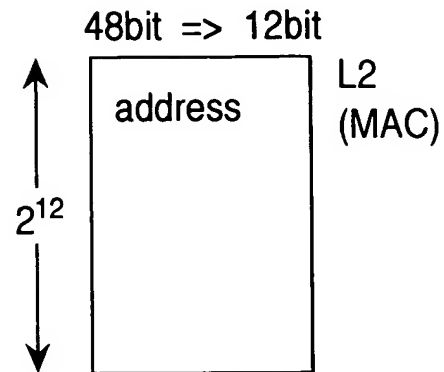


FIG. 7

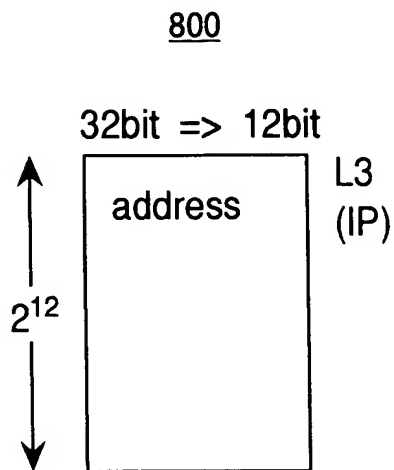


FIG. 8



900

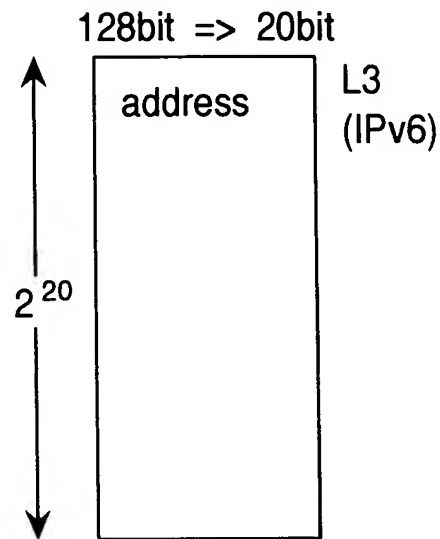


FIG. 9

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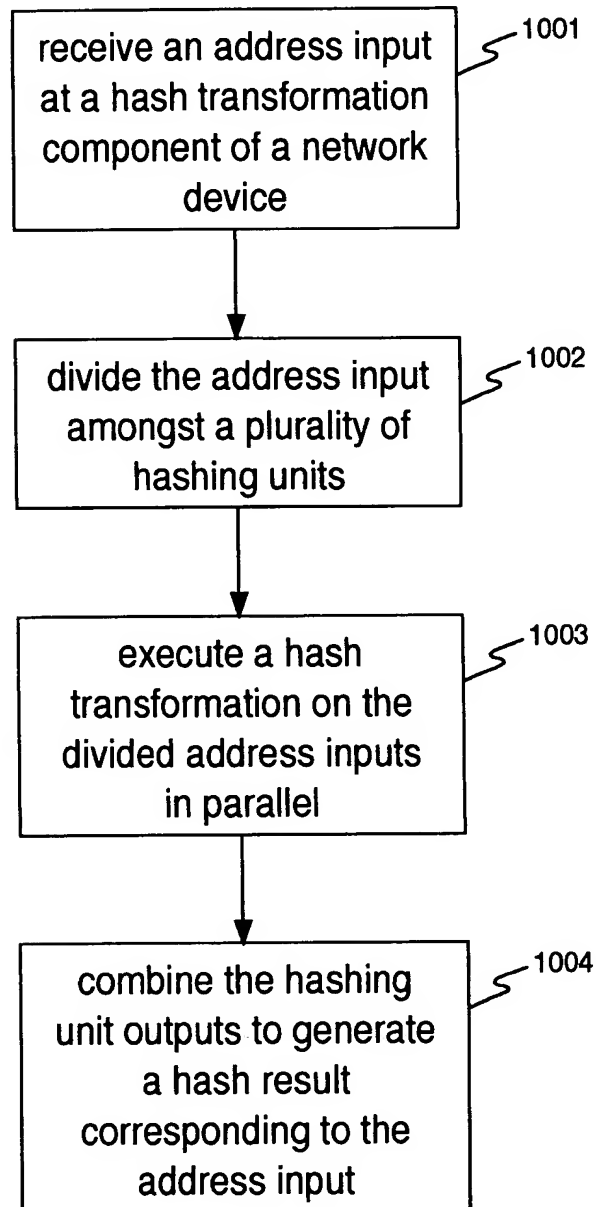
1000

FIG. 10